

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	184519	(input receiv\$10) same (memory buffer RAM ROM) same (output transmi\$10) same (controller processor)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/05/17 16:26
L2	308	370/235.ccls. and ((input receiv\$10) same (memory buffer RAM ROM) same (output transmi\$10) same (controller processor))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/05/17 16:27
L3	46	370/235.ccls. and ((input receiv\$10) same (memory buffer RAM ROM) same (output transmi\$10) same (controller processor)) and hierarch\$5	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/05/17 16:43
L4	9	370/230.1.ccls. and ((input receiv\$10) same (memory buffer RAM ROM) same (output transmi\$10) same (controller processor)) and hierarch\$5	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/05/17 16:44
L5	51	370/230.1.ccls. and ((input receiv\$10) same (memory buffer RAM ROM) same (output transmi\$10) same (controller processor))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/05/17 17:07
L6	145	370/232.ccls. and ((input receiv\$10) same (memory buffer RAM ROM) same (output transmi\$10) same (controller processor))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/05/17 17:36
L7	10	370/232.ccls. and ((input receiv\$10) same (memory buffer RAM ROM) same (output transmi\$10) same (controller processor)) and hierarch\$5	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/05/17 17:07
L8	259	370/230.ccls. and ((input receiv\$10) same (memory buffer RAM ROM) same (output transmi\$10) same (controller processor))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/05/17 17:38
L9	49	370/230.ccls. and ((input receiv\$10) same (memory buffer RAM ROM) same (output transmi\$10) same (controller processor)) and hierarch\$5	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/05/17 17:57
L10	101	370/412.ccls. and ((input receiv\$10) same (memory buffer RAM ROM) same (output transmi\$10) same (controller processor)) and hierarch\$5	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/05/17 18:25

L11	53	370/468.ccls. and ((input receiv\$10) same (memory buffer RAM ROM) same (output transmi\$10) same (controller processor)) and hierarch\$5	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/05/17 18:25
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